

<b>Notice of References Cited</b>		Application/Control No.	Applicant(s)/Patent Under Reexamination 10/662,233 KOVER ET AL.	
		Examiner	Art Unit	Page 1 of 1 Zeev Kitov 2836

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,545,513	04-2003	Tsuchida et al.	327/108
	B	US-4,216,393	08-1980	Gillberg et al.	326/88
	C	US-6,127,746	10-2000	Clemente, Stefano	307/131
	D	US-6,459,324	10-2002	Neacsu et al.	327/379
	E	US-6,271,708	08-2001	Hoshi et al.	327/377
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)		
	U	A. Sedra et al., Microelectronic Circuits, 1987, Holt, Reinhart and Winston, pp. 342-345, 408-410.		
	V			
	W			
	X			

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.